

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	28	714/34.ccls. and @pd>"20060214"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:11
L2	52	714/45.ccls. and @pd>"20060425"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:20
L3	41	714/39.ccls. and @pd>"20060502"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:25
L4	94	714/30.ccls. and @pd>"20060502"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:37
L5	52	717/128.ccls. and @pd>"20060502"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:44
L6	42	717/124.ccls. and (trace traces tracing traced) and @pd>"20060425"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:48
L7	2	(debug adj halt) and (program adj counter) and @pd>"20051227"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:54
S65	445	714/45.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:20
S68	5	714/34.ccls. and (debug adj halt)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/03 10:09

EAST Search History

S70	78	714/45.ccls. and (program adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/05 09:11
S71	262	714/34.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:10
S72	463	714/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:25
S73	804	714/30.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:36
S74	374	717/128.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:44
S75	195	717/124.ccls. and (trace traces tracing traced)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:48
S76	418	714/38.ccls. and (trace traces tracing traced)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/08 15:22
S77	6	714/30.ccls. and (debug adj halt)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 06:43
S78	56	(debug adj halt)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 07:49
S79	2	"6961872".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 07:37

EAST Search History

S80	26	(US-20040117770-\$ or US-20040158776-\$ or US-20040015880-\$ or US-20040019828-\$ or US-20020147965-\$).did. or (US-6918065-\$ or US-6912675-\$ or US-5771240-\$ or US-6311325-\$ or US-6530076-\$ or US-6094729-\$ or US-5978937-\$ or US-6000044-\$ or US-6058393-\$ or US-5764885-\$ or US-6985848-\$ or US-6463551-\$ or US-6922795-\$ or US-5165036-\$ or US-3707725-\$ or US-6961872-\$ or US-6195748-\$ or US-6684348-\$ or US-6754852-\$ or US-6981248-\$ or US-6766511-\$).did.	US-PGPUB; USPAT	OR	OFF	2006/06/12 06:56
S81	15	S80 and (program adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 06:57
S82	2	"20040128655"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 07:27
S83	410	debug\$4 with (program adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 07:27
S84	48	(debug adj halt) and (program adj counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 18:54
S85	3	"6912675".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 07:37
S86	2	"6912675".pn. and debug\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 07:37
S87	2	"5771240".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/12 07:49

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L7	2	(debug adj halt) and (program adj counter) and @pd>"20051227"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 19:04
L8	9	(timing adj trace adj apparatus).clm. and (program adj counter adj trace adj apparatus).clm. and (synchronization adj apparatus).clm. and (program adj counter adj trace adj apparatus).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 19:05
L9	1	(timing adj trace adj apparatus).clm. and (program adj counter adj trace adj apparatus).clm. and (synchronization adj apparatus).clm. and (program adj counter adj trace adj apparatus).clm. and (debug adj halt adj signal).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 19:07
L11	1	(timing adj trace adj stream).clm. and (program adj counter adj trace adj stream).clm. and (data adj trace adj stream).clm. and (debug adj halt adj signal).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 19:07
L12	10	(timing adj trace adj stream).clm. and (program adj counter adj trace adj stream).clm. and (data adj trace adj stream).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 19:09
L13	0	(debug adj halt adj signal).clm. and (reset adj signal).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/09 19:09

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"debug halt" "program counter"

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Web

Results 1 - 10 of about 374 for "**debug halt**" "**program counter**". (0.13 seconds)

[PDF] Xilinx XAPP571 DEBUGHALT Controller for PowerPC Boot and Reset ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... note is accompanied by a reference design that demonstrates **debug halt** ... Load the software into the caches and set the **program counter** to the start ...

www.xilinx.com/bvdocs/appnotes/xapp571.pdf - [Similar pages](#)

TI Code Composer Studio V2.x Primer

Run - starts execution of the program at the current **program counter** (PC). ... If the processor is running, halt it by doing: **DEBUG:HALT** or shift- F5 or ...

cnx.org/content/m12767/latest/ - 34k - [Cached](#) - [Similar pages](#)

Analog Devices : Embedded Processing & DSP : Technical Support ...

If this occurs the **program counter** will increase by one instruction and unexpected behavior will occur. Extra halts issued through automation or scripts ...

www.analog.com/processors/cda/epTASearchResult0,3001,,00.html - 961k -

[Cached](#) - [Similar pages](#)

Apparatus and method for trace stream identification of a ...

The packets also identify the **program counter** address at the time of the generation of the **DEBUG HALT** signal and relate the **debug halt** sync marker to a ...

www.patentdebat.com/PATAPP/20040117770 - Supplemental Result - [Similar pages](#)

Apparatus and method for capturing the **program counter** address ...

The **program counter** contents related to the trigger signal is delayed to ... For example, the trigger unit may initiate an interrupt, a **debug halt**, ...

www.patentdebat.com/PATAPP/20040177288 - Supplemental Result - [Similar pages](#)

Apparatus and method for trace stream identification of a ...

When a **DEBUG HALT** signal is generated in a target processor during a test procedure, a **debug halt** sync marker is generated in a **program counter** trace stream ...

www.freepatentsonline.com/20040117770.html - 51k - [Cached](#) - [Similar pages](#)

Separation of debug windows by IDS bit - Patent 6981178

moving the return address to the **program counter**, and entering a **debug halt** state if the interrupt during debug bit has the first state. ...

www.freepatentsonline.com/6981178.html - 51k - [Cached](#) - [Similar pages](#)

[[More results from www.freepatentsonline.com](#)]

Re: ppc 405 in **debug halt** mode

From: Peter Ryser; Subject: Re: ppc 405 in **debug halt** mode; Date: Wed, ... Typically, **program counter** information is collected by external tools through the ...

www.newsfeeds.com/archive/comp-arch-fpga/msg06198.html - 11k - Supplemental

Result - [Cached](#) - [Similar pages](#)

[PS] ELEC 434 Lab 1: TI Code Composer Studio V2.x Primer 1 Introduction

File Format: Adobe PostScript - [View as Text](#)

starts execution of the program at the current **program counter** (PC). ... 5. If the processor is running, halt it by doing: **DEBUG:HALT** ...

www-ece.rice.edu/~chol/elec434/files/tool5.ps - [Similar pages](#)

Session 8: which end of the pipe is broken?

The jump command lets you change the **program counter**. ... **debug> halt -p p2 debug>**

SUSPENSED p2 [_read] 0x8000ef6b (_read+12:) jae +0x0a <8000ef77> ...

osr600doc.sco.com/en/SDK_cdebug/_Session_8_Which_End_of_the_Pipe.html - 17k -

[Cached](#) - [Similar pages](#)